



PRİYADARSHINI COLLEGE OF ENGINEERING

(Approved by AICTE, New Delhi & Govt. Of Maharashtra,

Affiliated to R.T.M. Nagpur University, Nagpur)

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One Page Report of Students Activity

Name of Department: Electronics and Telecommunication Engineering

Title of the Activity: Content Beyond Syllabus (Differences between Intel i3, i5 & i7 Processors)

Date/Time/Venue: 01/03/2025 / 12.30pm / Classroom

Name of the Faculty/Coordinator/Resource persons: Dr.V.V. Panchbhai

Resources used: Power Point Presentation

Brief Description of Activity: The objective of the session was to provide students with practical knowledge about real-world processor architectures beyond academic curriculum. Key aspects such as core count, clock speed, hyper-threading, cache memory, power efficiency, and performance bench-marking were discussed in detail. The session helped students understand the suitability of different processors for various computing needs and applications.


Number of Students Involved: 23 Out of 29


Outcome: Students understood the key architectural and performance differences between Intel i3, i5, and i7 processors. The session enhanced their ability to relate theoretical concepts with real-world processor applications and improved their awareness of current industry technologies.

Relevance to POs, PSOS:

PO 1, 2, 3, 10, 12 &

PSO 1, 2


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